

UNITED STATES PATENT APPLICATION

FOR

**LINE INTERFACE, APPARATUS AND METHOD FOR COUPLING
TRANSCIVER AND TRANSMISSION LINE**

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Attorney Docket Number: LSI-01-160

Client Docket Number: 01-160

SPECIFICATION

TITLE OF INVENTION

LINE INTERFACE, APPARATUS AND METHOD FOR COUPLING TRANSCEIVER AND TRANSMISSION LINE

FIELD OF THE INVENTION

[0001] The present invention relates to digital subscriber line (DSL) technologies. More particularly, the present invention relates to a low-voltage analog line interface for an asymmetrical digital subscriber line (ADSL) transceiver system.

BACKGROUND OF THE INVENTION

[0002] In an ADSL transceiver, the circuitry connecting between an integrated circuit of an analog front end (AFE) and the transmission line is referred to as an analog line interface. An AFE is a circuit block that provides the interface between the line transceiver and a digital signal processing (DSP) processor. FIG. 1 illustrates a typical hybrid structure of a conventional analog interface of an ADSL system. As shown in FIG. 1, an analog line interface typically includes a line driver 1, a hybrid circuit 3, a receive amplifier 5, and a transformer 7. The transmit signal (TX) is typically coming from a DSP portion 8 (such as transmit shaping filters) of the ADSL transceiver through a digital to analog converter (DAC) of AFE (not shown). The transmit signal is then supplied with sufficient voltage and current by the line driver 1, and coupled via the hybrid circuit 3 and the transformer 7 to a transmission line 9, such as a telephone line or

D **B** **E** **L** **I**

[0005] First, it is desirable to integrate the TX line driver into an AFE chip so as to minimize the cost. This normally implies that the TX line driver has to operate at a low supply voltage, for example, 5V or 3.3V. However, since the ADSL transmit signal on

the line is typically required to have about 15 volts peak-to-peak differential (V_{ppd}), it is necessary to step-up the transmit signal by a turns ratio of, for example, $n = 4.25$.

[0006] Furthermore, it is desirable to reduce the number of different voltage supplies needed to implement a DSL transceiver. Thus, it is also desirable that the line driver operates at the same supply voltage, for example, as the AFE chip, instead of its conventional voltage supply of 10-12 V which is required to drive the transmit signal without a step-up (i.e., the turns ratio of 1).

[0007] In addition, reducing the power required for a line driver is desirable, for example, in the Universal Serial Bus (USB)-based customer premises equipment (CPE) modems, such as a modem integrated into a personal computer (PC).

[0008] As mentioned above, however, a step-up of the transmit signal means the corresponding step-down of the receive signal, which degrades the SNR of the receive signal by the same amount. Such a degraded noise performance of a line interface directly affects the data rate of the transceiver. Accordingly, it would be desirable to build a line interface that can simultaneously allow a line driver operating from a low power supply voltage and not incur the severe noise penalty associated with the traditional line interface with a hybrid circuit.

BRIEF DESCRIPTION OF THE INVENTION

[0009] A line interface couples signals between a data transceiver and a transmission line having a load impedance Z . The line interface includes a transformer, a driver circuit for supplying a transmit signal from the data transceiver to the transformer, and a receiver circuit for receiving a receive signal from the transformer. The transformer includes a first port coupled to the transmission line, a second port coupled to the driver circuit, a third port coupled to the receiver circuit, a first winding part having a turns ratio of $1:n$, where $n > 1$, for coupling the transmit signal from the second port to the first port, and a second winding part having a turns ratio of $1:m$, where $m < n$, for coupling receive signal from the first port to the third port.

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BRIEF DESCRIPTION OF THE DRAWINGS

[0010] The accompanying drawings, which are incorporated into and constitute a part of this specification, illustrate one or more embodiments of the present invention and, together with the detailed description, serve to explain the principles and implementations of the invention.

[0011] In the drawings:

[0012] FIG. 1 is a diagram illustrating a typical hybrid structure of a conventional analog interface of an ADSL system.

[0013] FIG. 2 is a diagram schematically illustrating a multiport transformer for a low-voltage integrated line driver in accordance with the present invention.

[0014] FIG. 3A is a diagram illustrating a line interface including a multiport transformer in accordance with a specific embodiment of the present invention.

[0015] FIG. 3B is a diagram illustrating a conventional line interface including a conventional transformer and an inverting type RX amplifier.

[0016] FIG. 4 is a diagram illustrating an example of a line interface having a differential structure in accordance with a specific embodiment of the present invention.

[0017] FIG. 5A is a diagram illustrating a line interface including the multiport transformer and a non-inverting type RX amplifier in accordance with a specific embodiment of the present invention.

[0018] FIG. 5B is a diagram illustrating a conventional line interface including a conventional transformer and a non-inverting type RX amplifier.

[0019] FIG. 6A is a diagram illustrating a line interface with an active termination architecture, including the multiport transformer and an inverting type RX amplifier in accordance with a specific embodiment of the present invention.

[0020] FIG. 6B is a diagram illustrating a conventional line interface with an active termination architecture, including a conventional transformer and an inverting type RX amplifier.

[0021] FIG. 7 is a diagram illustrating a line interface with an active termination architecture, including the multiport transformer and a non-inverting type RX amplifier in accordance with a specific embodiment of the present invention.

[0022] FIG. 8A is a diagram illustrating a line interface including the multiport transformer and a receive amplifier implementing a difference function directly, in accordance with a specific embodiment of the present invention.

[0023] FIG. 8B is a diagram illustrating a line interface including a conventional transformer and a receive amplifier implementing a difference function directly.

[0024] FIG. 9A is a diagram illustrating a line interface including the multiport transformer and a receive amplifier implementing a difference function directly, in accordance with a specific embodiment of the present invention.

[0025] FIG. 9B is a diagram illustrating a line interface including a conventional transformer and a receive amplifier implementing a difference function directly.

[0026] FIG. 9C is a diagram illustrating a line interface with active termination including the multiport transformer and a receive amplifier implementing a difference function directly, in accordance with a specific embodiment of the present invention.

[0027] FIG. 10 is a diagram schematically illustrates a signal coupling method in a line interface in accordance with a specific embodiment of the present invention.

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DETAILED DESCRIPTION

[0028] Embodiments of the present invention are described herein in the context of a line interface and an apparatus and method for coupling a transceiver and a transmission line. Those of ordinary skill in the art will realize that the following detailed description of the present invention is illustrative only and is not intended to be in any way limiting. Other embodiments of the present invention will readily suggest themselves to such skilled persons having the benefit of this disclosure. Reference will now be made in detail to implementations of the present invention as illustrated in the accompanying drawings. The same reference indicators will be used throughout the drawings and the following detailed description to refer to the same or like parts.

[0029] In the interest of clarity, not all of the routine features of the implementations described herein are shown and described. It will, of course, be appreciated that in the development of any such actual implementation, numerous implementation-specific decisions must be made in order to achieve the developer's specific goals, such as compliance with application- and business-related constraints, and that these specific goals will vary from one implementation to another and from one developer to another. Moreover, it will be appreciated that such a development effort might be complex and time-consuming, but would nevertheless be a routine undertaking of engineering for those of ordinary skill in the art having the benefit of this disclosure.

[0030] FIG. 2 schematically illustrates a new structure of a transformer **10** for a low-voltage integrated line driver, according to the present invention. The transformer **10** and

a low-voltage line driver are implemented in an ADSL line interface of the present invention. As shown in FIG. 2, The transformer 10 couples the transmit signal (TX) and receive signal (RX) between a transceiver and a transmission line.

[0031] The transformer 10 is a multiport transformer and includes a first port (Line port) 12, a second port (TX port) 14, and a third port (RX port) 16. The first port is adapted to being coupled to the transmission line (with an impedance Z). The second port 14 is adapted to being coupled to a driver circuit of the transceiver, and the third port 16 is adapted to being coupled to a receiver circuit of the transceiver. The transmit signal (TX) is coupled from the second port 14 to the first port 12 through a first path having a step-up ratio of n . Typically, $n > 1$ for providing actual step-up of the transmit signal. On the other hand, the receive signal (RX) is coupled from the first port 12 to the third port 16 through a second path having the step-down ratio of $1/m$, where $m < n$. That is, the multiport transformer 10 has two turns ratio parameters n and m : n is the step-up parameter seen by the TX signal, and m is the step down parameter seen by the RX signal. Typically, m is set to be 1 so that the receive signal is coupled without a step-down. However, m may be smaller than 1 so as to step "up" the receive signal.

[0032] It should be noted that although m is referred as a "step-down" parameter m , the second path may couple the receive signal without a step-down, or may steps up the receive signal, as described above. Similarly, although the second path is described to have the "step-down" ratio of $1/m$, when m is equal to or smaller than 1, the second path actually couples the receive signal without a step-down or provides a step-up to the

receive signal. The step-up ratio (of the first path) means a coupling ratio along the transmit signal path, and the step-down ratio (of the second path) means a coupling ratio along the receive signal path, which is in the opposite direction of the transmit signal path.

[0033] FIG. 3A illustrates a line interface 30 including a transformer 20 in accordance with a specific embodiment of the present invention. The line interface 30 also includes an inverting type receive amplifier 32 in accordance with this specific embodiment. As shown in FIG. 3A, the transformer 20 includes a first winding part 21 having a turns ratio of $1:n$, and a second winding part 22 having a turns ratio of $1:m$. The first winding part 21 is coupled between a TX port 24 and a Line port 26, and the second winding part 22 is coupled between the Line port 26 and a RX port 28. It should be noted that the input resistors of the receive amplifier 32 have resistance R and resistance $2mR/n$ to provide a first-order cancellation of the simultaneously occurring transmit signal from the receiving path.

[0034] In order to compute the noise performance of the entire system of the AFE and the line interface 30, two parameters G_{RX} and G_n are calculated. The quantity G_n is the noise gain for the receive amplifier noise V_n , referred to the transmission line, i.e., an effective noise at the Line port 26. The quantity G_{RX} is the signal gain for the receive signal from the line to the output of the receive amplifier 32. When the receiver amplifier 32 has a feedback resistance of R_F , as shown in FIG. 3A, these parameters are expressed as follows:

$$G_{RX} = \frac{1}{m} \cdot \frac{R_F}{R} \quad (1)$$

$$G_n = \frac{1}{G_{RX}} + m + \frac{n}{2} \quad (2)$$

The lower the noise G_n , the better data rate of the entire system is achieved.

[0035] For comparison, FIG. 3B illustrates a conventional line interface **34** including a conventional transformer **36** and an inverting receive amplifier **38**. For this conventional structure, the parameters G_{RX} and G_n are calculated as follows:

$$G_{RX} = \frac{1}{n} \cdot \frac{R_F}{R} \quad (3)$$

$$G_n = \frac{1}{G_{RX}} + n + \frac{n}{2} \quad (4)$$

Comparing Equation (1) with Equation (3), and Equation (2) with Equation (4), since the turns ratio $m < n$, the present invention achieves a higher receive signal gain and a lower noise gain, providing a better SNR for the receive signal.

[0036] FIG. 3A shows a single-ended representation of the line interface **30** for simplicity. However, an actual implementation of the line interface **30** may have a double-ended (differential) structure, as is well understood by those of ordinary skill in the art. FIG. 4 illustrates an example of a line interface **40** having a differential structure in accordance with a specific embodiment of the present invention. The line interface **40** includes a multiport transformer **42**. As show in FIG. 4, the multiport transformer **42** has a TX port **44**, a Line port **46**, and a RX port **48**, and provides a step-up ratio n for the

transmit signal and a step-down ratio m for the receive signal. The receiving path is illustrated with a hybrid circuit, a receive amplifier, and a highpass filter circuit.

[0037] FIG. 5A illustrates a line interface **50** including the transformer **20** and a non-inverting type receive amplifier **52** in accordance with a specific embodiment of the present invention. The structure of the transformer **20** is the same as described above. Although FIG. 5A shows a single-ended representation of the line interface **50** for simplicity, an actual implementation of the line interface **50** may have a double-ended (differential) structure similar to the line interface **40**, as is well understood by those of ordinary skill in the art. For the line interface **50**, the gain parameters G_{RX} and G_n are calculated as follows:

$$G_{RX} = (1 + \frac{R_F}{R_S}) \cdot \frac{1}{m + \frac{n}{2}} \quad (5)$$

$$G_n = m + \frac{n}{2} \quad (6)$$

where R_S is a coupling resistance to a reference voltage (ground).

[0038] For comparison, FIG. 5B illustrates a conventional line interface **54** including a conventional transformer **36** and a non-inverting receive amplifier **58**. For this conventional structure, the parameters G_{RX} and G_n are calculated as follows:

$$G_{RX} = (1 + \frac{R_F}{R_S}) \cdot \frac{1}{(\frac{3n}{2})} \quad (7)$$

$$G_n = \frac{3n}{2} \quad (8)$$

Comparing Equation (5) with Equation (7), and Equation (6) with Equation (8), the present invention achieves a higher receive signal gain and a lower noise gain, providing a better SNR for the receive signal.

[0039] It is also possible to use a positive feedback in the line interface to achieve a matching impedance. FIG. 6A illustrates a line interface 60 including the multiport transformer 20, an inverting type RX amplifier 62, a TX line driver 64, and a positive feedback path 66 having a gain of $(1 - K)$ coupled to the input of the TX line driver 64. In this active termination architecture, the resistance of a back termination resistor R_T is reduced by factor K , where $K < 1$, thereby decreasing the power dissipation due to the back termination resistor R_T . Although FIG. 6A shows a single-ended representation of the line interface 60 for simplicity, an actual implementation of the line interface 60 may have a double-ended (differential) structure, as is well understood by those of ordinary skill in the art.

[0040] FIG. 6B illustrates, for comparison, a conventional line interface 68 with an active termination architecture, including a conventional transformer 36. The line interface 60 according to the present invention also achieves a lower noise gain and a higher receive signal gain than that of the corresponding conventional structure with an active termination in a similar manner as described above.

[0041] In addition, a line interface with an active termination may have a non-inverting RX amplifier instead of the inverting RX amplifier 62. As shown in FIG. 7, in

accordance with a specific embodiment of the present invention, the line interface **70** includes the multiport transformer **20** and a non-inverting RX amplifier, and has an active termination architecture. Although FIG. 7 shows a single-ended representation of the line interface **70** for simplicity, an actual implementation of the line interface **70** may have a double-ended (differential) structure, as is well understood by those of ordinary skill in the art.

[0042] FIG. 8A illustrates a line interface **80** having a new configuration having the multiport transformer **20** where a receive amplifier **82** implements a difference function directly, in accordance with a specific embodiment of the present invention. In this configuration, the turns ratios n and m of the transformer **20** must satisfy $n < 2m$. For the line interface **80**, the gain parameters G_{RX} and G_n are calculated as follows:

$$G_{RX} = \frac{1}{m} \left(1 + \frac{R_F}{R} \right) \quad (9)$$

$$G_n = m \quad (10)$$

[0043] The configuration of the line interface **80** achieves a lower noise gain G_n than other configurations discussed above. For example, compared with Equation (2) (noise gain of the line interface **30** shown in FIG. 3A) and Equation (6) (noise gain of the line interface **50** shown in FIG. 5A), Equation (10) yields the lower noise gain for a given turns ratio n and a given receive signal gain G_{RX} , which are typically predetermined parameters in real applications.

[0044] FIG. 8B illustrates, for comparison, a conventional line interface **84** having a conventional transformer **36**, where a receive amplifier **86** implements a difference function directly. For the conventional line interface **84**, the gain parameters G_{RX} and G_n are calculated as follows:

$$G_{RX} = \frac{1}{n} \left(1 + \frac{R_F}{R} \right) \quad (11)$$

$$G_n = n \quad (12)$$

As is apparent from comparing Equations (9) and (10) with Equations (11) and (12), the line interface **80** according to the present invention also achieves a lower noise gain and a higher receive signal gain than that of the corresponding conventional structure with a receive amplifier implementing a direct difference function.

[0045] Having two turns ratios n and m gives a freedom to achieve a required G_{RX} . In order to cancel the transmit signal in a receive path, the feedback resistance R_F and the input resistance R of the receive amplifier must satisfy a specific relationship. For example, in the line interface **80** shown in FIG. 8A, the feedback resistance R_F is given as:

$$R_F = \frac{\frac{n}{2m}}{1 - \frac{n}{2m}} \cdot R \quad (13)$$

Thus, from Equation (9), the receive signal gain G_{RX} is expressed as:

$$G_{RX} = \frac{1}{m - \frac{n}{2}} \quad (14)$$

[0046] However, in the conventional line interface **84** shown in FIG. 8B, the receive signal gain is given as $G_{RX} = \frac{2}{n}$, since $R_F = R$ for the transmit signal cancellation.

Therefore, there is no freedom to control the receive signal gain G_{RX} for a fixed parameter of the turns ratio n in the conventional configuration. The freedom of controlling receive signal gain G_{RX} is similarly obtained in other configurations of the line interface in accordance with the present invention, as is indicated by the parameter m in the corresponding receive signal gain equations described above.

[0047] FIG. 9A illustrates a line interface **90** having another new configuration having the multiport transformer **20** where a receive amplifier **92** implements a difference function directly, in accordance with a specific embodiment of the present invention. In this configuration, the turns ratios n and m of the transformer **20** must satisfy $n > 2m$. For the line interface **90**, the gain parameters G_{RX} and G_n are calculated as follows:

$$G_{RX} = \frac{1}{m - \frac{n}{2}} \quad (15)$$

$$G_n = \frac{n}{2} \quad (16)$$

[0048] The configuration of the line interface **90** achieves a lower noise gain G_n than that of the line interface **80** for a given turns ratio n and a given receive signal gain G_{RX} , because the noise gain G_n of the line interface **80** is also expressed as, from Equations

(10) and (14), $G_n = \frac{1}{G_{RX}} + \frac{n}{2}$. Therefore, the configuration of the line interface **90** yields the lowest noise gain among the above examples.

[0049] FIG. 9B illustrates, for comparison, a corresponding line interface configuration **94** having a conventional transformer **36**, where a receive amplifier **96** implements a difference function directly in a similar manner as the receive amplifier **92** of the line interface **90**. For this configuration, the receive signal gain G_{RX} is given as $G_{RX} = -\frac{1}{n} \cdot \frac{R_F}{R}$. However, the output signal V_o of the receive amplifier **96** for a transmit signal voltage V_{TX} and a receive signal voltage V_{RX} is calculated as:

$$\begin{aligned} V_o &= \left(1 + \frac{R_F}{R}\right) \cdot V_{TX} - \frac{R_F}{R} \left(\frac{V_{TX}}{2} + \frac{V_{RX}}{n}\right) \\ &= \left(1 + \frac{R_F}{2R}\right) \cdot V_{TX} - \frac{R_F}{R} \cdot \frac{1}{n} \cdot V_{RX} . \end{aligned}$$

Thus, there is no way to cancel the transmit signal from the receive path in this configuration having a conventional transformer **36**, while the present invention with the multiport transformer **20** realizes the lowest noise gain as well as the transmit signal cancellation.

[0050] FIG. 9C illustrates a line interface **100** with active termination in accordance with a specific embodiment of the present invention. The configuration of the line interface **100** shows the implementation of the concept illustrated in FIG. 9A for the case of active termination. Similarly to the case of conventional line interface configuration **94** (shown in FIG. 9B) described above, the transmit signal cannot be cancelled in a

corresponding conventional configuration with active termination using a conventional transformer.

[0051] In examples discussed above, the hybrid circuit and line interface structures have been shown using resistors to implement gain functions around the receive amplifier and to achieve the hybrid subtraction. However, capacitors, instead of resistors, may also be used in all of these structures, and similar noise reduction and signal gain increase can be achieved using the multiport transformer of the present invention. In the case of capacitive hybrid structure, the equivalent impedance is used instead of resistance and a proper DC biasing circuit can be employed, as is well understood by those of ordinary skill in the art.

[0052] As described above, the multiport transformer of the present invention, combined with various types of hybrid circuit, line driver, and receive amplifier circuits, realizes a low-voltage line interface having an improved noise performance. By adding a second winding part to a conventional step-up transformer, a receive signal path without an undesirable step-down is made available for the receive signal. Although the multiport transformer of the present invention is described mainly referring to the transformer 20, the configuration of the multiport transformer is not limited to those structures shown in the corresponding drawings, and includes any transformer configuration that realizes a first step-up coupling for the transmit signal and a second coupling for the receive signal without an undesirable step-down. That is, the receive signal is coupled to the transceiver without a step-down, with a step-down smaller than

the standard step-down that should be if the receive signal is coupled via the same coupling path as the transmit signal, or with a step-up.

[0053] In an ADSL interface application of the present invention, the multiport transformer is combined with a low voltage line driver. For example, the multiport transformer is combined with an integrated single chip including an AFE and a low voltage line driver, such as a single 5V supply voltage line driver, realizing a low-voltage line interface with an improved noise performance. The present invention is very advantageous in such a combination of the multiport transformer and a line driver with a single low-voltage supply. It should be noted that the receive amplifier may be internal to the AFE or external to the AFE. In addition, the receive amplifier may be omitted entirely, and in such a case the receive circuit has a passive hybrid structure.

[0054] As described above, the present invention applies to ADSL interfaces, and specifically to the G.992.1 standard. However, the present invention is not limited to ADSL or a specific standard, but is applicable to any DSL systems, i.e., xDSL. Furthermore, the present invention may also be applied to any transceiver systems such as Ethernet. In addition, although the present invention is explained with single-ended circuit structures for simplicity, the present invention applies to both single-ended and differential circuit structures.

[0055] According to the present invention, signals between a transceiver and a transmission line are coupled with a step-up ratio n for a transmit signal, and with a step-

down ratio $1/m$ for a receive signal via a multiport transformer. FIG. 10 schematically illustrates a signal coupling method in accordance with a specific embodiment of the present invention. The multiport transformer may be the transformer 10 or 20 as described above. The multiport transformer has a TX port, a RX port, and a Line port.

[0056] First, a transmit signal (TX) from the driver circuit is supplied to the TX port of the transformer (101). A first path from the TX port to the Line port is provided for the transmit signal to couple the transmit signal to the transmission line (103). The first path through the transformer has a step-up ratio of n . The first path may include a first winding part of the transformer having a turns ratio of n .

[0057] When a receive signal (RX) comes from the transmission line, it is received at the Line port of the transformer (105), and a second path from the Line port to the RX port is provided (107) so as to couple the receive signal to the receive circuit of the transceiver. The second path has a step-down ratio of $1/m$, where $m < n$. The second path may include a second winding part of the transformer having a turns ratio of m . By selecting the value of m smaller than that of n , for example, $m = 1$, the undesirable step-down of the receive signal is reduced or eliminated. By choosing $m < 1$, a step-up of the receive signal is also possible.

[0058] While embodiments and applications of this invention have been shown and described, it would be apparent to those skilled in the art having the benefit of this disclosure that many more modifications than mentioned above are possible without

departing from the inventive concepts herein. The invention, therefore, is not to be restricted except in the spirit of the appended claims.

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